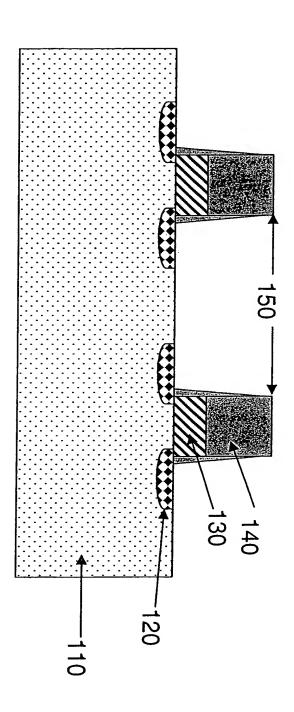
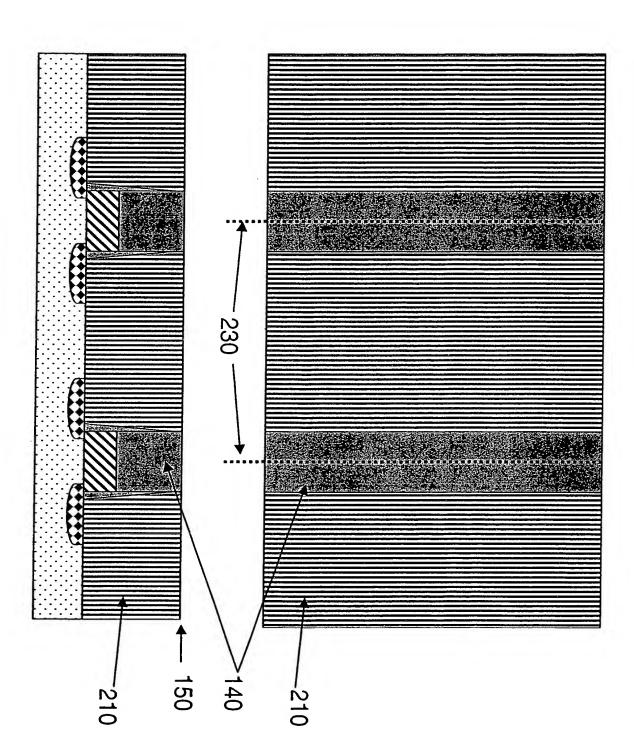
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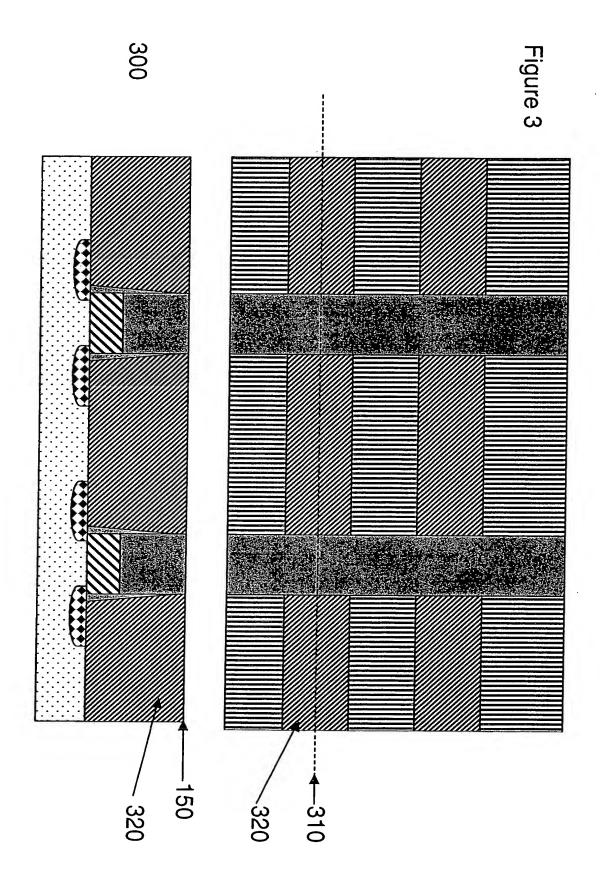




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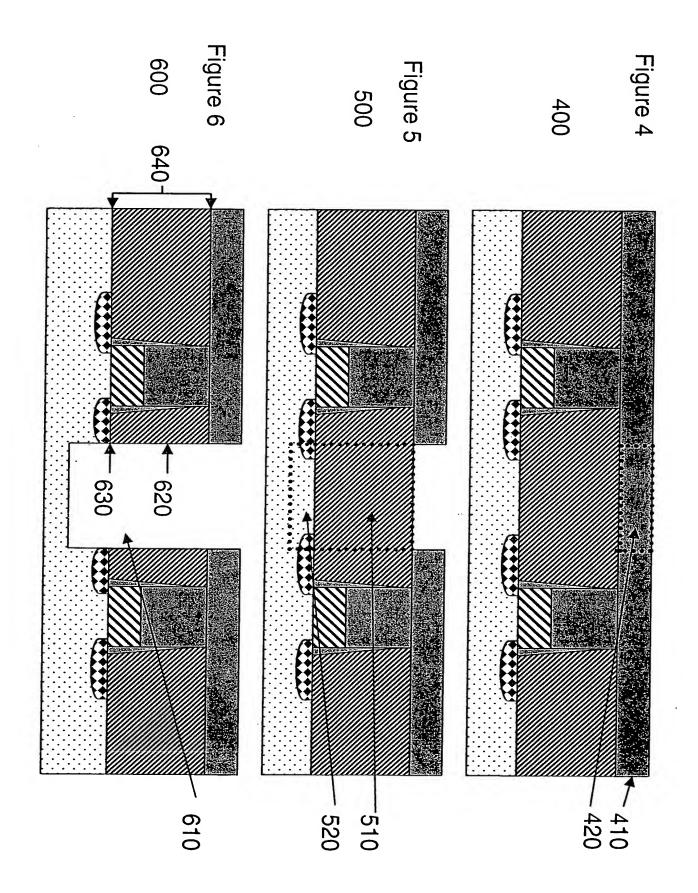
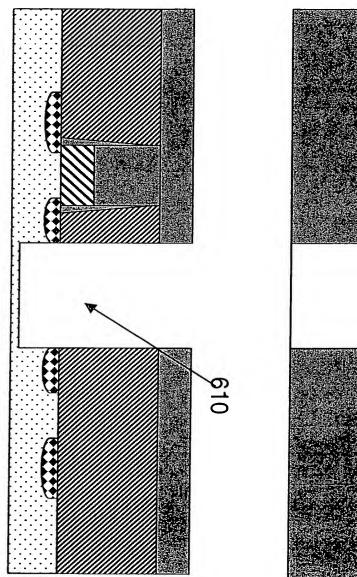
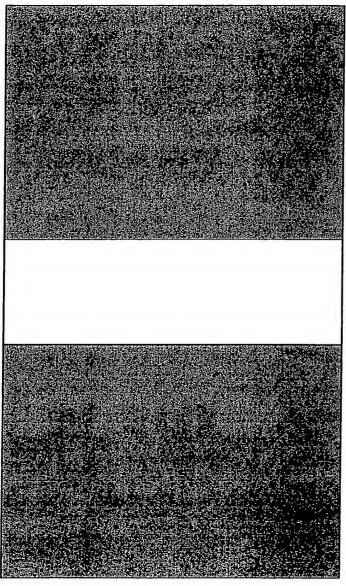
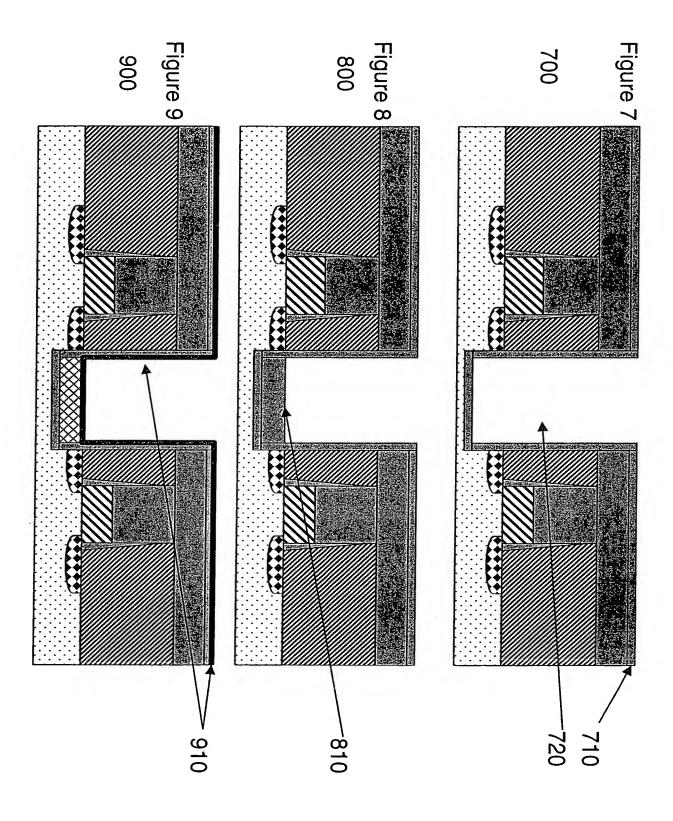


Figure 6/





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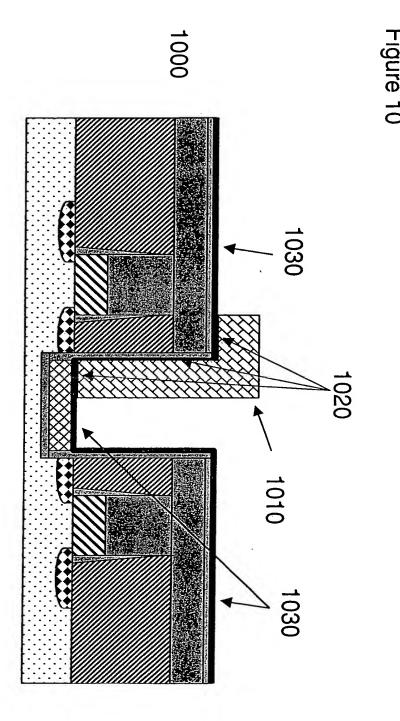


Figure 12

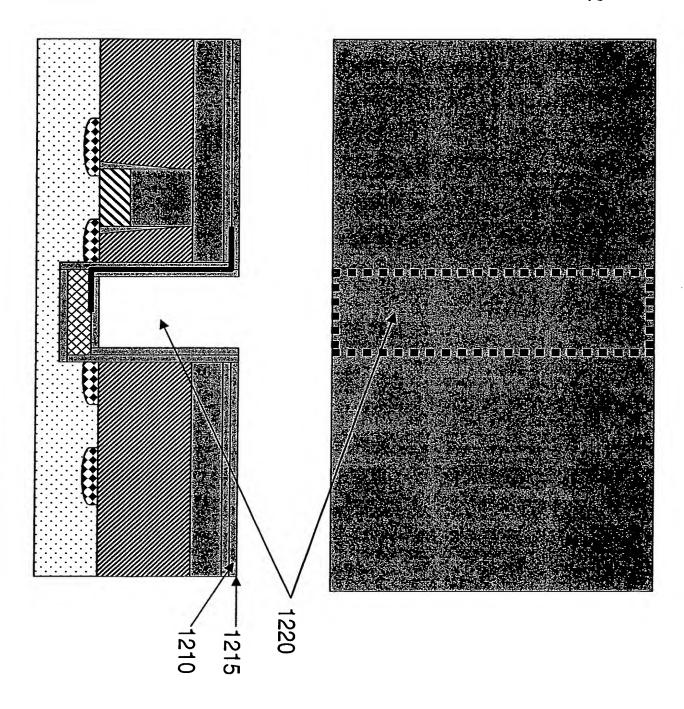
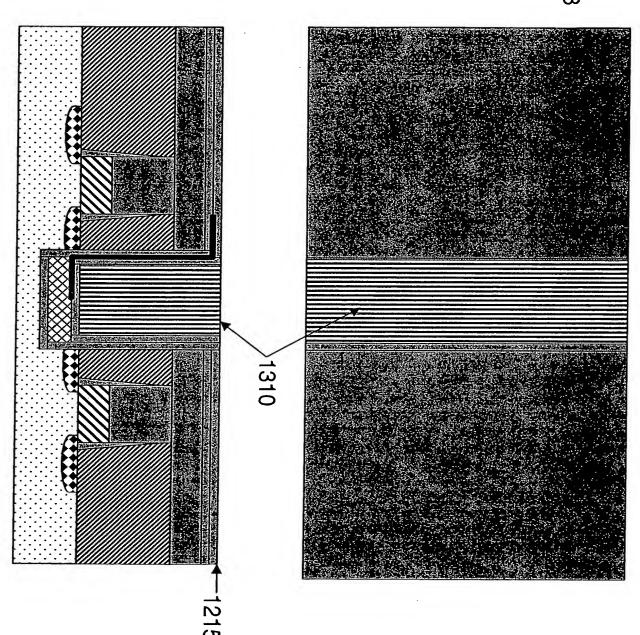
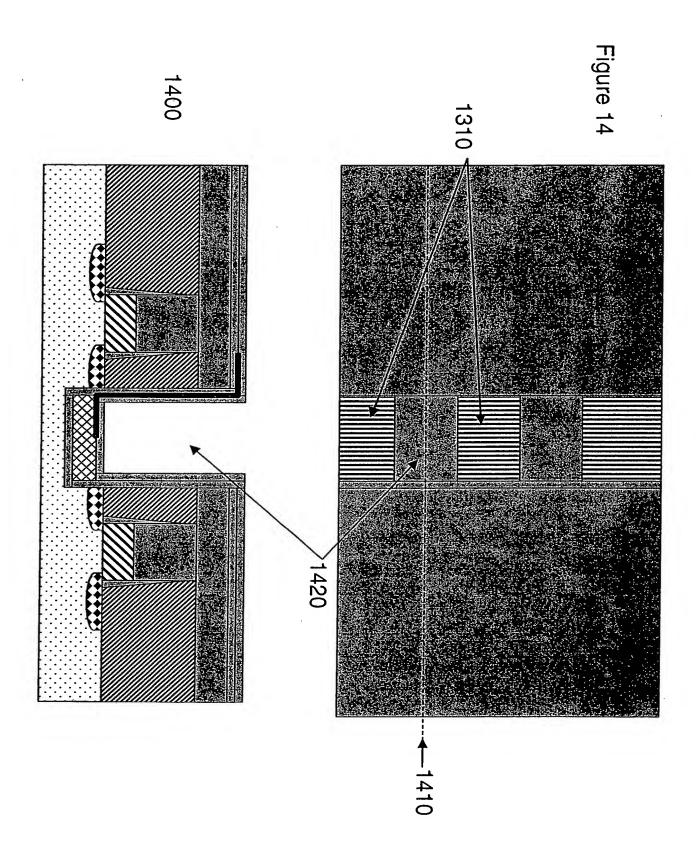


Figure 13



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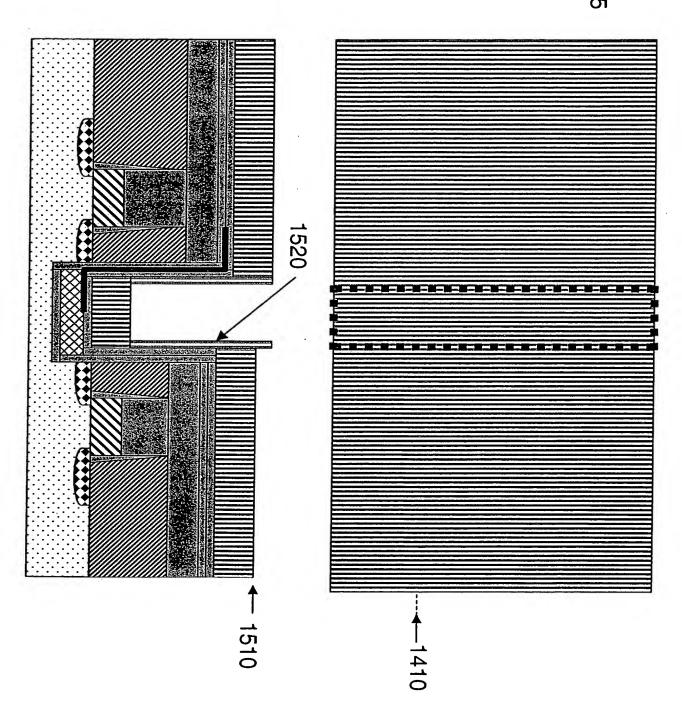
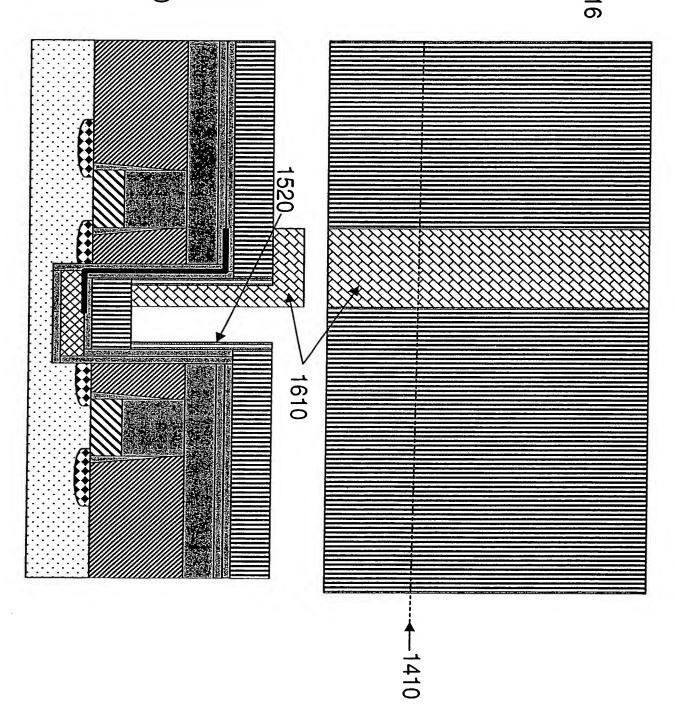


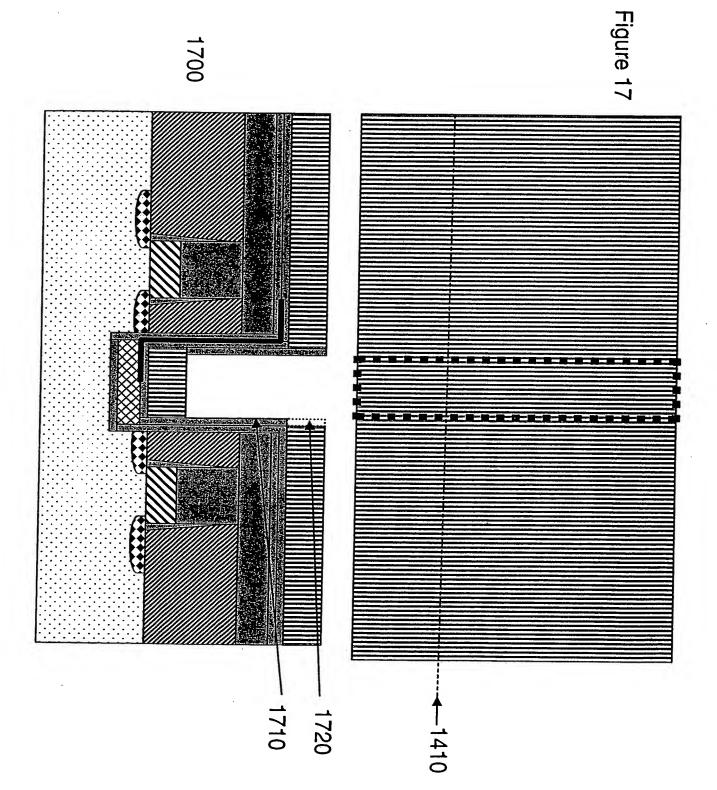
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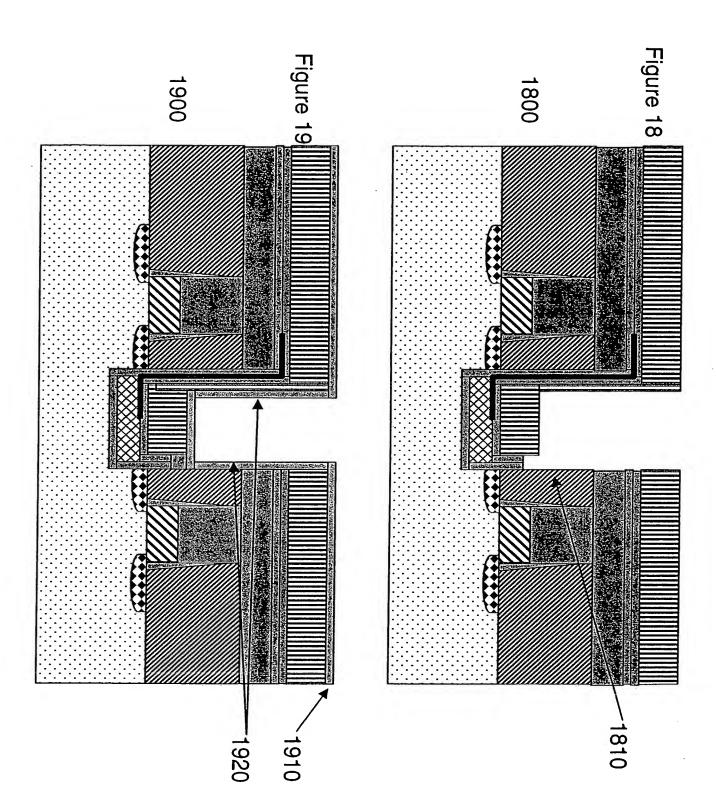


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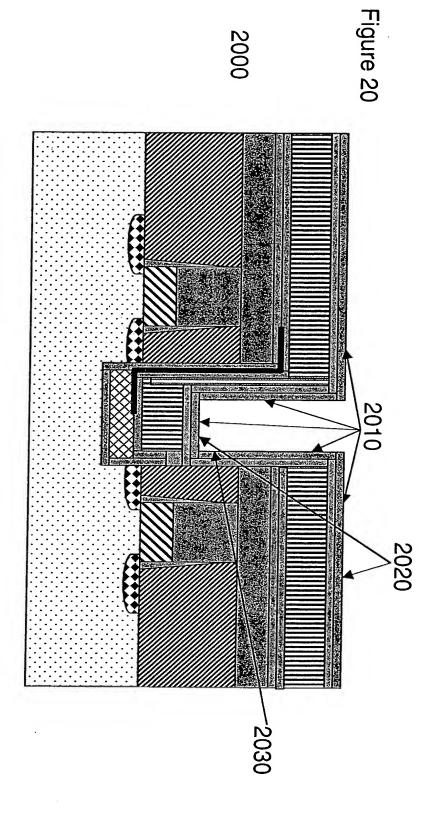
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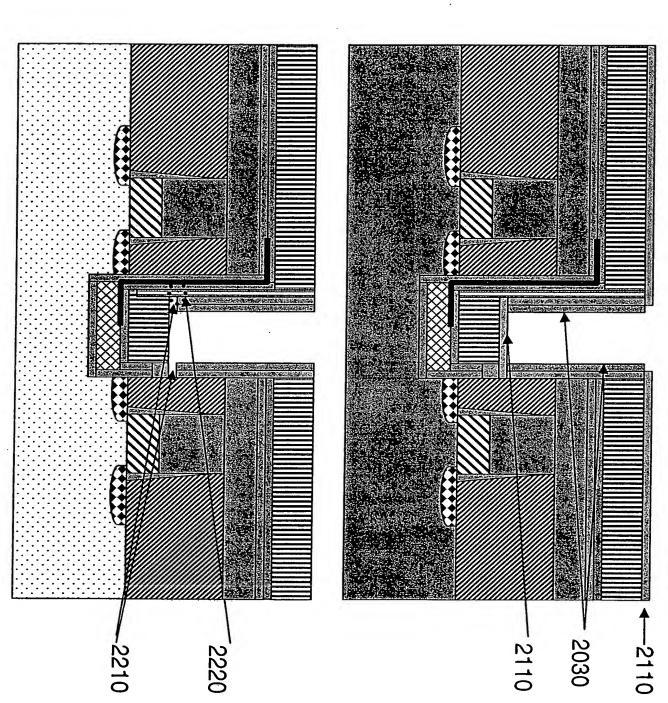


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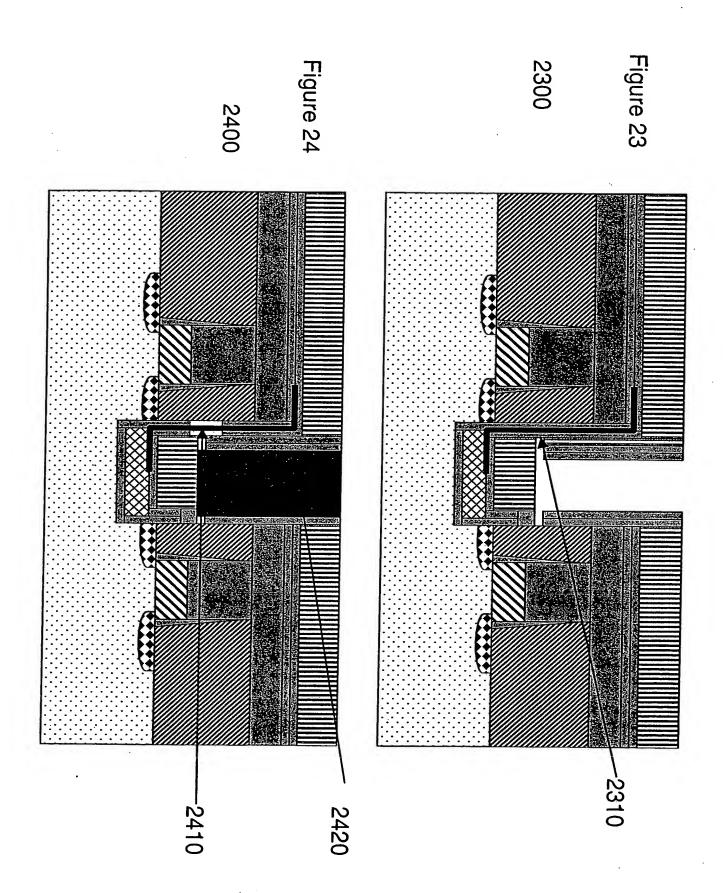


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Figure 21 2200



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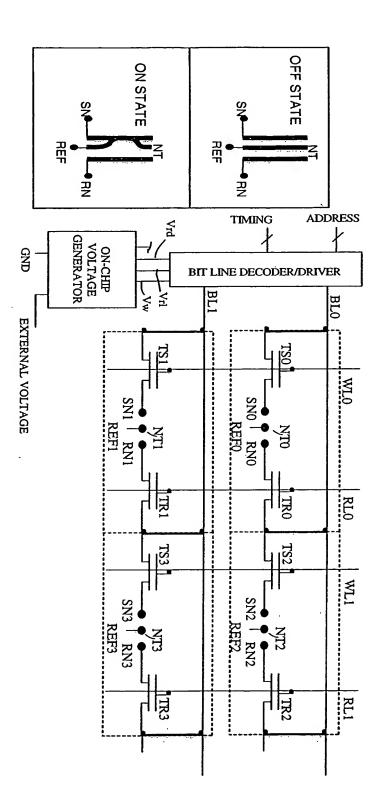


Figure 25

Bit erasable 12 F2 cell layout